MAT LAB & VLSI PROJECTS LIST

S. No.	CODE	Project Title
		IMAGE PROCESSING
1	LSDIP001	Histogram of Oriented Lines for Palm Print Recognition
2	LSDIP002	QR Images: Optimized Image Embedding in QR Codes
3	LSDIP003	A DCT Approximation with Low Complexity for Image Compression
4	LSDIP004	A new filter for removal of salt and pepper noise
5	LSDIP005	A New Retrieval Method Based on YUV Color Information for Digital Libraries
6	LSDIP006	Automatic License Plate Recognition (ALPR): A State-of-the-Art Review
7	LSDIP007	Data hiding using Steganography
8	LSDIP008	Dual Transform Based Steganography Using Wavelet Families and Statistical Methods
9	LSDIP009	Effective Watermarking Algorithm to Protect Electronic Patient Record Using Image Transform
10	LSDIP010	Evaluating Image Steganography Techniques: Future Research Challenges
11	LSDIP011	Hiding Secret Image in Video
12	LSDIP012	Image Compression Using Lifting Based Wavelet Transform Coupled With SPIHT Algorithm
13	LSDIP013	Image Retrieval using Both Color and Texture Features
14	LSDIP014	Image Steganography in DWT Domain using Double Stegging with RSA Encryption
15	LSDIP015	Multi Secure and Robustness for Medical Image Based Steganography Scheme
16	LSDIP016	Performance Evaluation of Traditional And Adaptive Lifting Based Wavelets With SPIHT for LOSSY Image Compression
17	LSDIP017	Properties of Digital Image Watermarking
18	LSDIP018	Robust Watermarking of AES Encrypted Images For DRM Systems
19	LSDIP019	Securing Information Using Steganography
20	LSDIP020	Security Attacks on the Wavelet Transform and Singular Value Decomposition Image watermarking
21	LSDIP021	Speech Steganography using LSB coding analysis
22	LSDIP022	SPIHT Algorithm Combined with Huffman Encoding
23	LSDIP023	Voice Verification System Using Wavelets
24	LSDIP024	Mining Weakly Labeled Web Facial Images for Search-Based Face Annotation
25	LSDIP025	Latent Fingerprint Matching Using Descriptor-Based Hough Transform
26	SBIMP04	Phase Adaptive Super Resolution of Mammographic Images Using Complex Wavelets
27	SBIMP07	Image Quality Assessment Based on Multi Scale Geometric Analysis
28	SBIMP10	Image Segmentation via Spline Regression
29	SBIMP18	Morphological Background detection
30	SCIMP03	High Resolution Cerebral Blood Flow Imaging by Registered Laser Speckle Contrast Analysis
31	SCIMP05	Scanned Compound Document Encoding Using Multiscale Recurrent Patterns
32	SCIMP07	Tracking and Activity Recognition Through Consensus in Distributed Camera Networks
33	SCIMP08	Turbo Pixel Segmentation Using Eigen Image
34	SCIMP12	Image Segmentation by MAP ML Estimations

MAT LAB & VLSI PROJECTS LIST

35	SCIMP14	Fast Query for Exemplar based Image Completion
36	SCIMP17	Phase Adaptive Super Resolution of Mammography Images using Complex Wavelets
37	SCIMP20	A Fast Multilevel Algorithm for Wavelet Regularized Image Restoration
38	SCIMP21	Morphological Background Detection and Enhancement of Images with Poor Lighting
39	SCIMP22	High Fidelity Data Embedding for Image Annotation
40	SCIMP25	Upper surface of the diaphragm estimate using 3D CT Images
41	SCIMP27	Automatic Exudates Detection from the eyes of Diabetic Patients
42	SCIMP28	Image Retrieval using Color, Texture and Shape
43	SCIMP29	Face Detection using Adaboost Algorithm
44	SCIMP30	Color Image Segmentation
45	SDIMP09	Boundary Detection in Medical Images Using Edge Following Algorithm Based on Intensity Gradient and Texture Gradient Features
46	SDIMP11	Multichannel Image Registration by Feature Based Information Fusion
DIGITAL SIGNAL PROCESSING		
47	SCDSP01	Energy Efficient Multi Object Tracking in Sensor Networks
48	SCDSP02	Cooperative Diversity of Spectrum Sensing for Cognitive Radio Systems
49	SDDSP01	QR Decomposition-Based Matrix Inversion for High Performance Embedded MIMO Receivers
COMMUNICATION		
50	SBCM01	Communication coverage for wireless passive networks
51	SCCM13	Channel Coding for High Speed Links: a Systematic Look at Code Performance and System Simulation
52	SCCM14	Training Design for Repetitive Slot based CFO Estimation in OFDM
53	SDCM03	Symbol-Level Synchronization and LDPC Code Design for Insertion/Deletion Channels
54	SDCM04	Optimized Differential GFSK Demodulator
55	SDCM06	Coded Free-Space Optical Links over Strong Turbulence and Misalignment Fading Channels
CORE VLSI		
56	SBVL01	A Fast Hardware Approach for Approximate, Efficient Logarithm and Antilogarithm Computations
57	SBVL04	A Low-Power Low-Area Multiplier Based on Shift-and-Add Architecture
58	SCVL01	On Clustering of Undetectable Single Stuck-At Faults and Test Quality in Full-Scan Circuits
59	SCVL03	A Multibank Memory-Based VLSI Architecture of DVB Symbol De-inter Leaver
60	SCVL08	A Fast VLSI Design of SMS4 Cipher Based on Twisted BDD S-Box Architecture
61	SCVL11	A Full-Adder-Based Methodology for the Design of Scaling Operation in Residue Number System
62	SCVL17	Field Programmable Gate Array Prototyping of End Around Carry Parallel Prefix Tree Architectures
63	SDVL01	Accumulator Based 3-Weight Pattern Generation(Testing)
64	SDVL11	Fixed-State Tests for Delay Faults in Scan Designs

MAT LAB & VLSI PROJECTS LIST

65	SDVL13	Mapping Multi-Domain Applications Onto Coarse-Grained Reconfigurable Architectures.
IMAGE PROCESSING		
66	SBVL09	Spread Spectrum Image Watermarking with Digital Design
67	SBVL13	A Compact AES Encryption Core on Xilinx FPGA
68	SCVL24	An Improved RC6 Algorithm with the Same Structure of Encryption and Decryption
COMMUNICATION		
69	SBVL17	A New Low Power Test Pattern Generator Using a Variable-Length Ring Counter
70	SBVL20	Fault Secure Encoder and Decoder for Nano Memory Applications
71	SCVL26	New Architectural Design of CA-Based Codec
72	SCVL28	Design Space Exploration of Hard-Decision Viterbi Decoding: Algorithm and VLSI Implementation
73	SDVL20	Design of an Error Detection and Data Recovery Architecture for Motion Estimation Testing Applications

CONTACT: ARUNKUMAR (040-40274843, 9533694296)
MAIL ID: logicsystemsprojects@gmail.com
WEBSITE: www.logicsystems.org.in